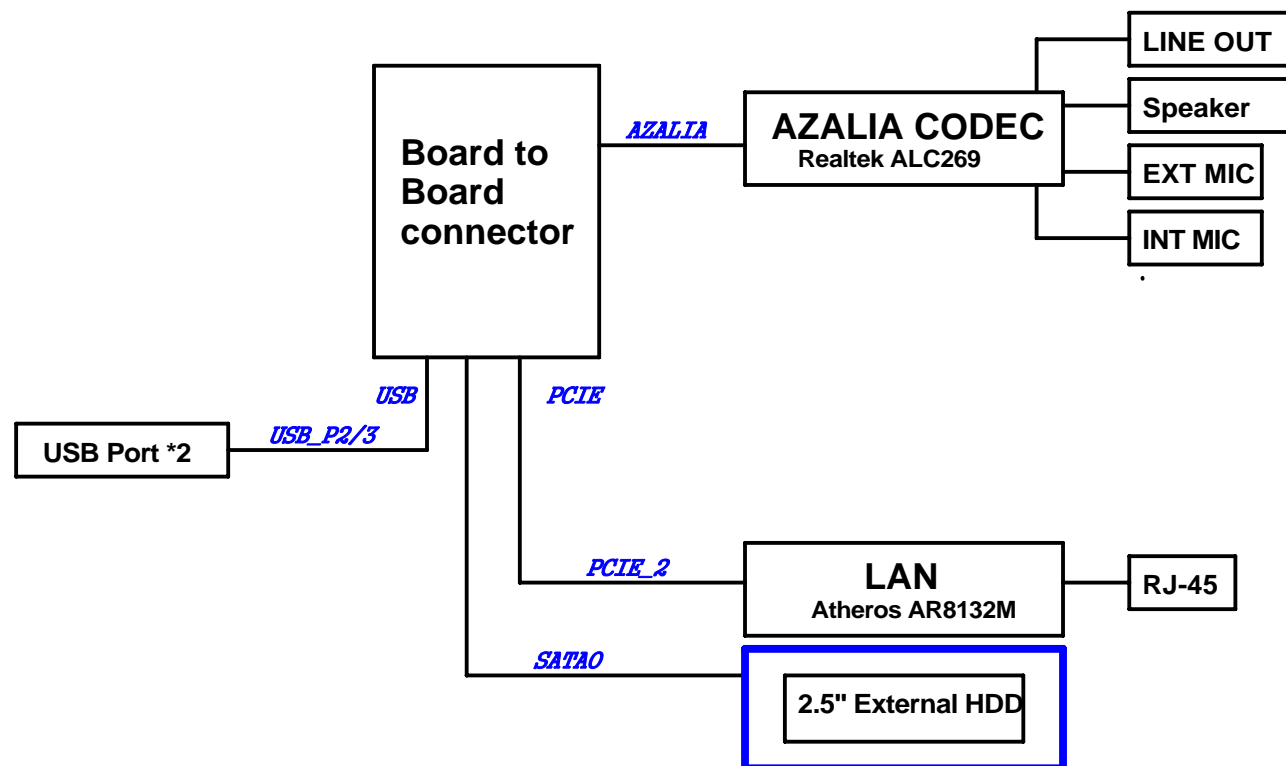


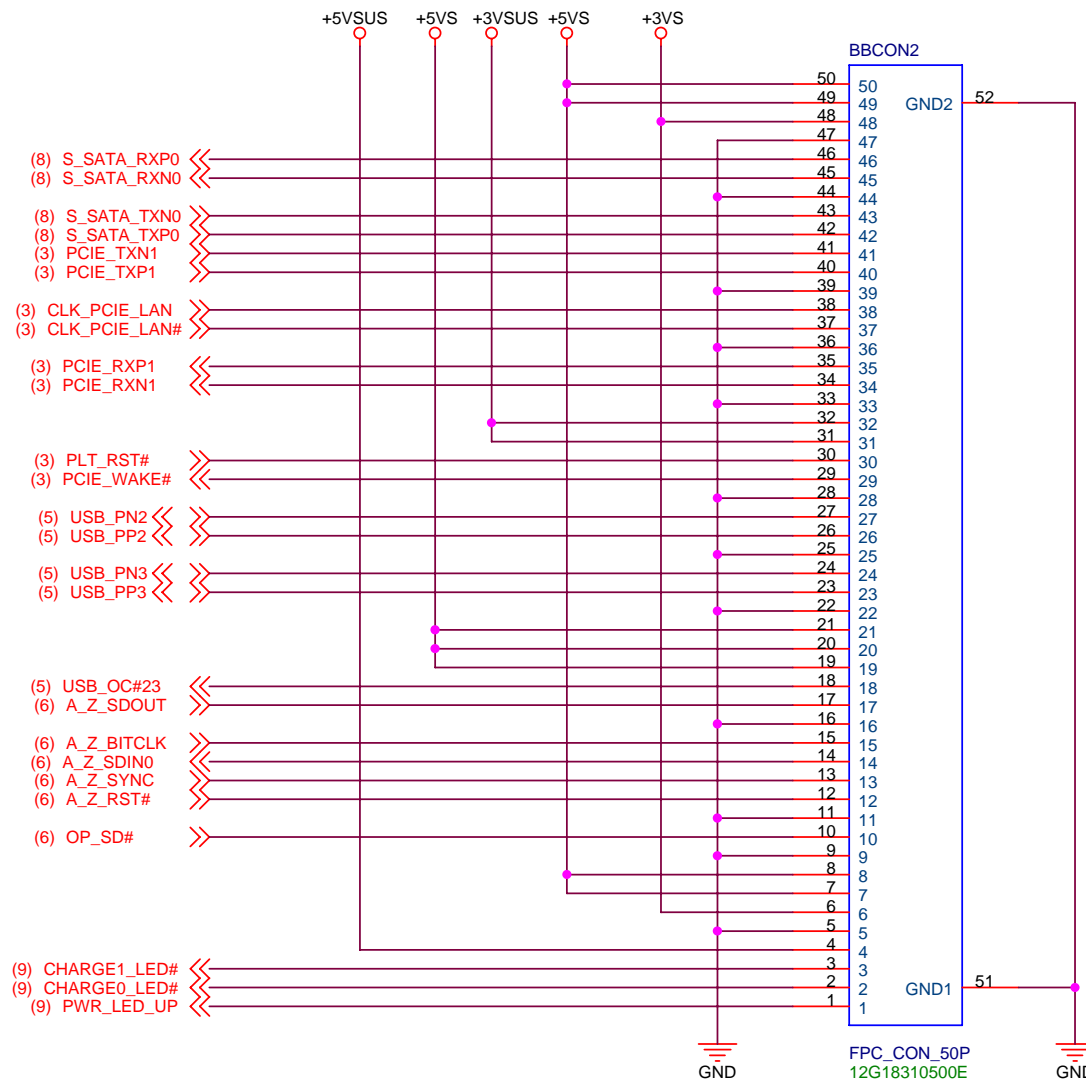
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| 01_Block Diagram |
| 02_B to B connector |
| 03_LAN_Atheros AR8132 |
| 04_MDC_RJ11_RJ45 |
| 05_USB Port |
| 06_Codec_ALC269 |
| 07_Audio_AMP_Jack |
| 08_SATA HDD |
| 09_Screw Hole |
| 10_EMI |
| 11_History |

MK90H_IO_BOARD_{1.1G}

2009_0420_1025

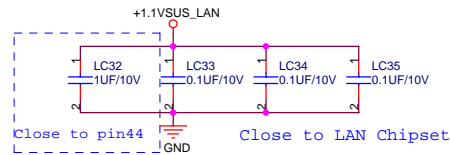
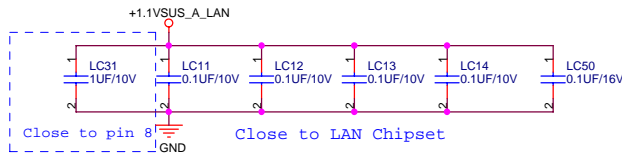
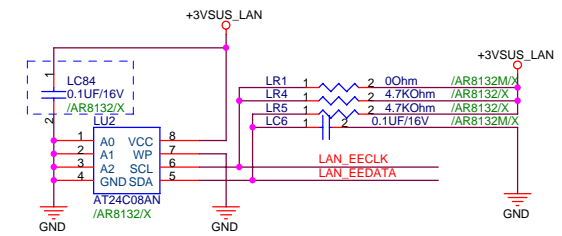
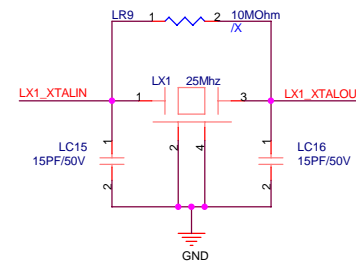
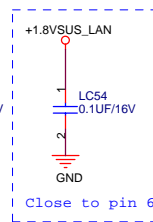
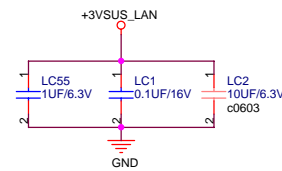
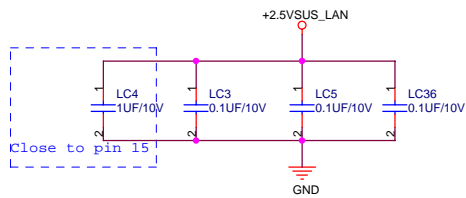


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|------------------------------|----------------|--|------------------------|--|-----------------------|--|
| <Core Design> | | | ASUS® | | Title : Block Diagram | |
| ASUSTek Computer INC. | | | Engineer: Regin Chiang | | | |
| Size | Project Name | | Rev | | | |
| A3 | MK90H_IO_BOARD | | 1.1G | | | |
| Date: Monday, April 20, 2009 | Sheet 1 of 12 | | | | | |



<Core Design>

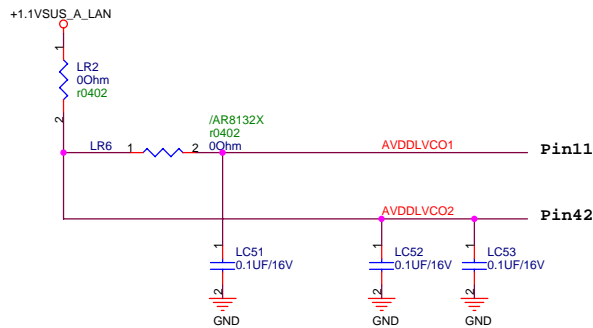
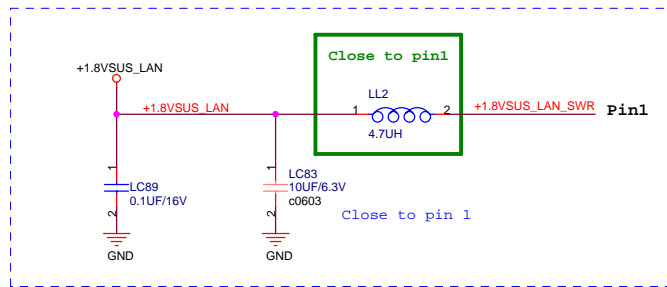
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| ASUS | | Title : B to B connector | |
| ASUSTek Computer INC. | | Engineer: Boison_Hung | |
| Size A4 | Project Name MK90H_IO_BOARD | | Rev 1.1G |
| Date: Monday, April 20, 2009 | | Sheet 2 | of 12 |



Close to pin45

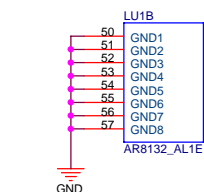
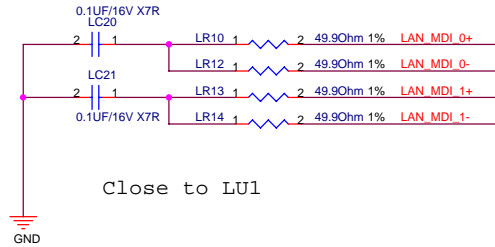
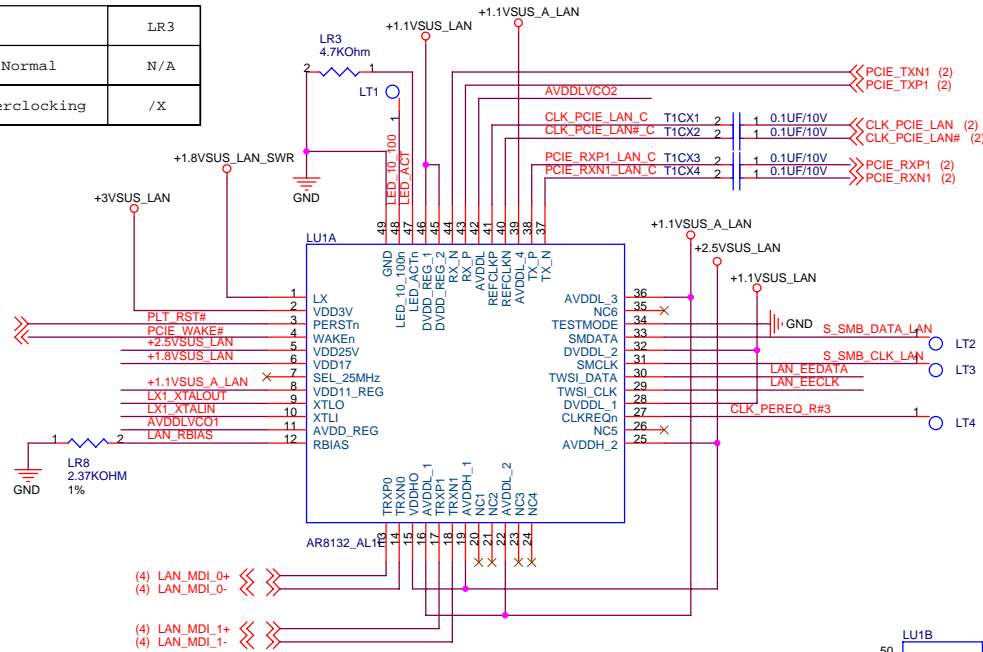


| | |
|--------------|-----|
| | LR3 |
| Normal | N/A |
| Overclocking | /X |



| | | |
|--------------|-----|-----|
| | LR2 | LR6 |
| Normal | N/A | /X |
| Overclocking | /X | N/A |

(2) PLT_RST#
(2) PCIE_WAKE#



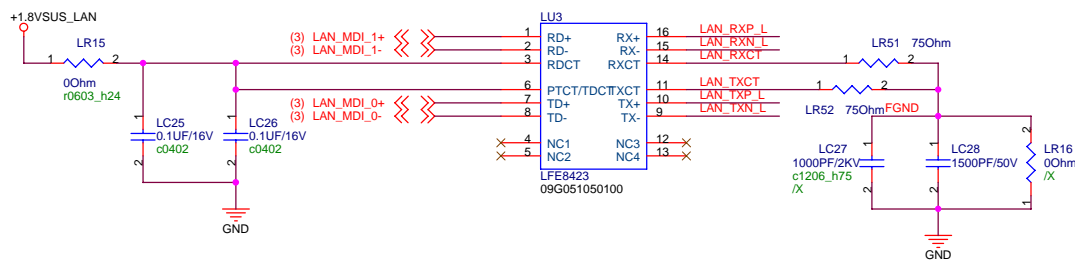
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ASUS Title : AR8113

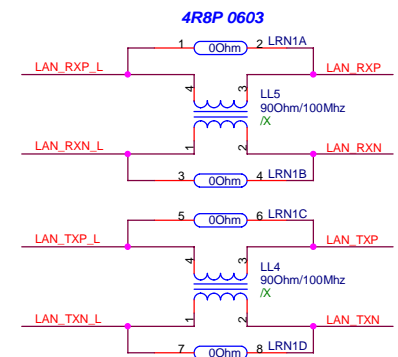
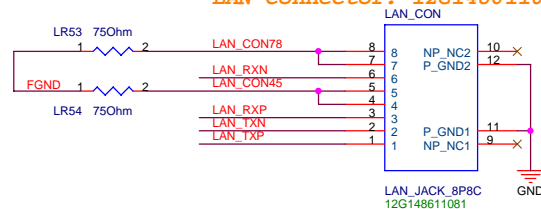
ASUSTek Computer INC Engineer: Jenen_wang

Size A3 Project Name MK90H IO BOARD Rev 1.1G

Date: Monday, April 20, 2009 Sheet 3 of 12



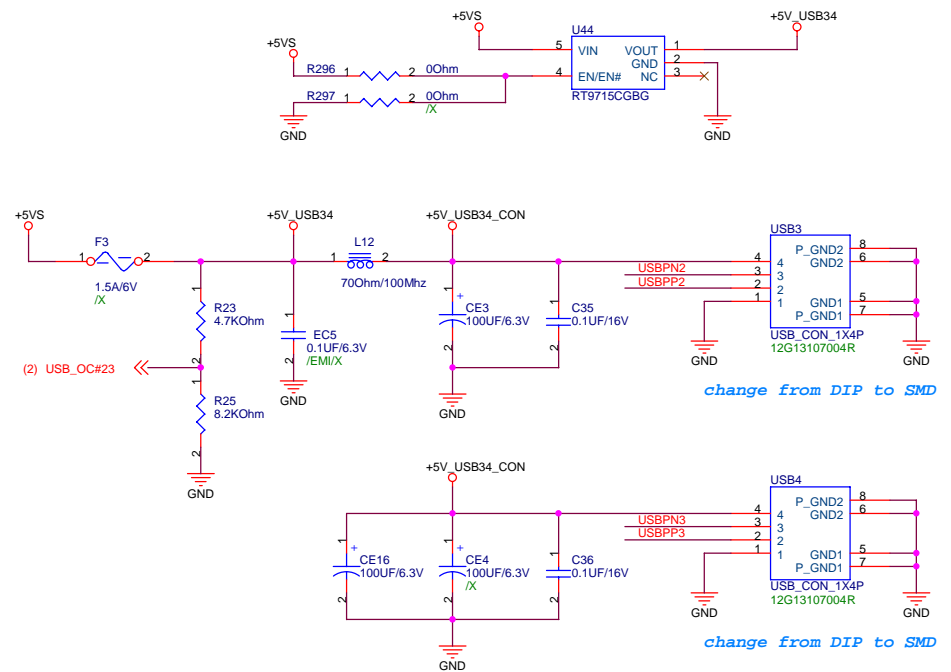
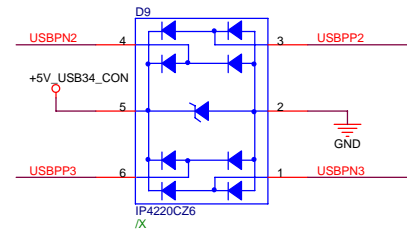
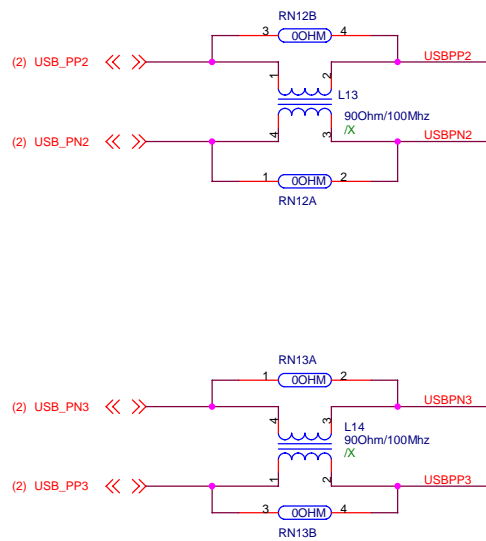
LAN connector: 12G148611081



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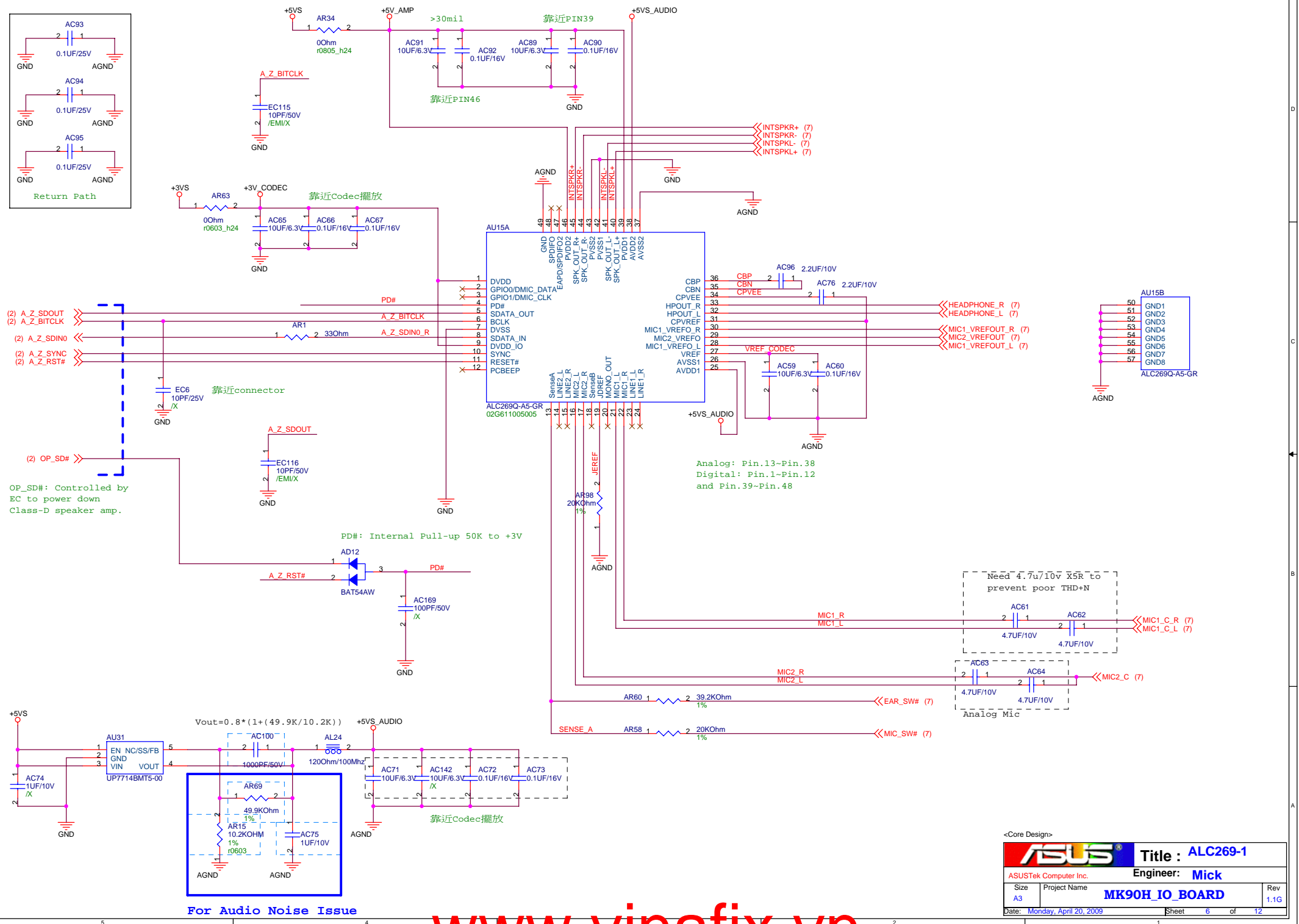
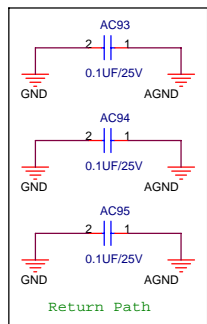
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| ASUS | | Title : MDC_RJ11_RJ45 | |
| ASUSTek Computer Inc. | | Engineer: Kell Huang | |
| Size | Project Name | Rev | |
| A3 | MK90H_IO_BOARD | 1.1G | |
| Date: Monday, April 20, 2009 | Sheet 4 of 12 | | |

External USB port 2,3

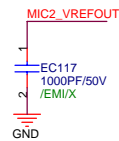
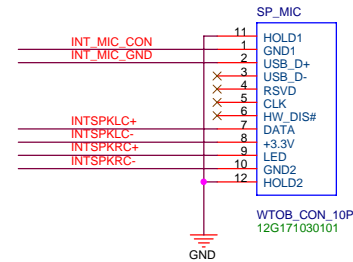
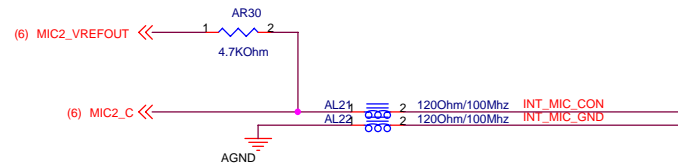


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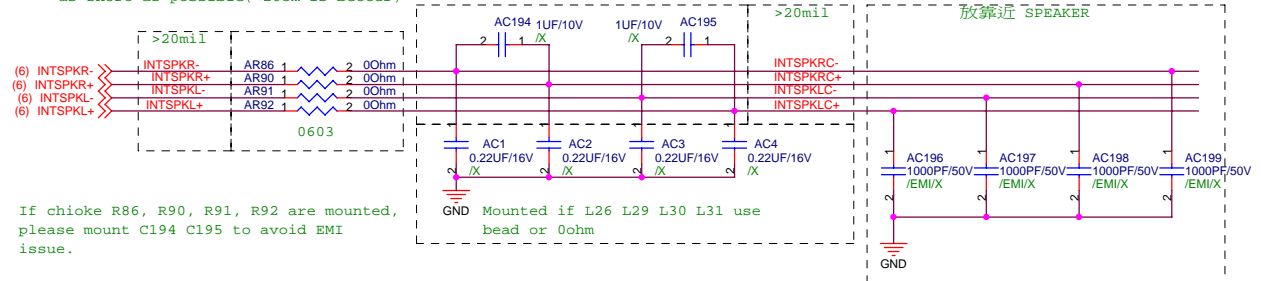
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| ASUS | | Title : USB Port | |
| ASUSTek Computer Inc. | | Engineer: Kell Huang | |
| Size A3 | Project Name MK90H_IO_BOARD | Rev 1.1G | |
| Date: Monday, April 20, 2009 | Sheet 5 | of 12 | |



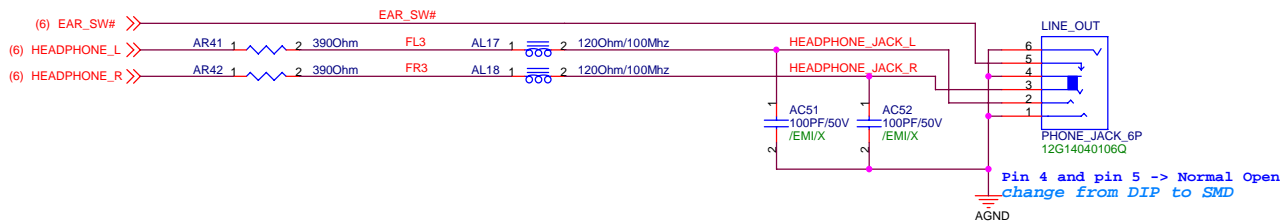
Internal Analog Mic



Total length from speakerR+- L+- (pin40 41 44 45) to internal speaker please as short as possible(<20cm is better)

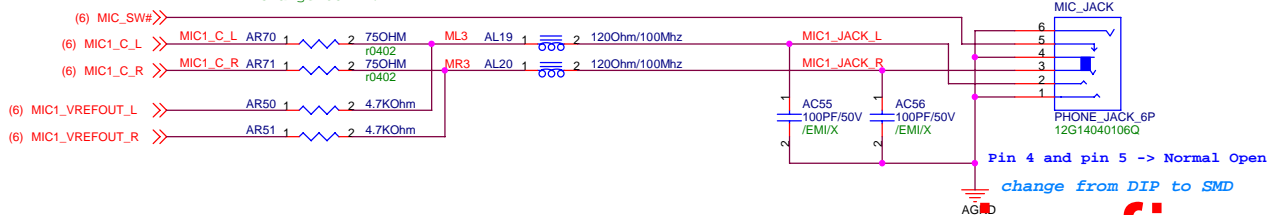


LINE_OUT use 12G14040106Q

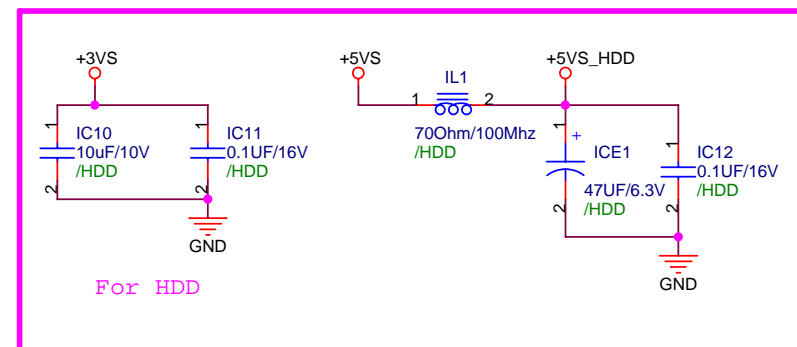
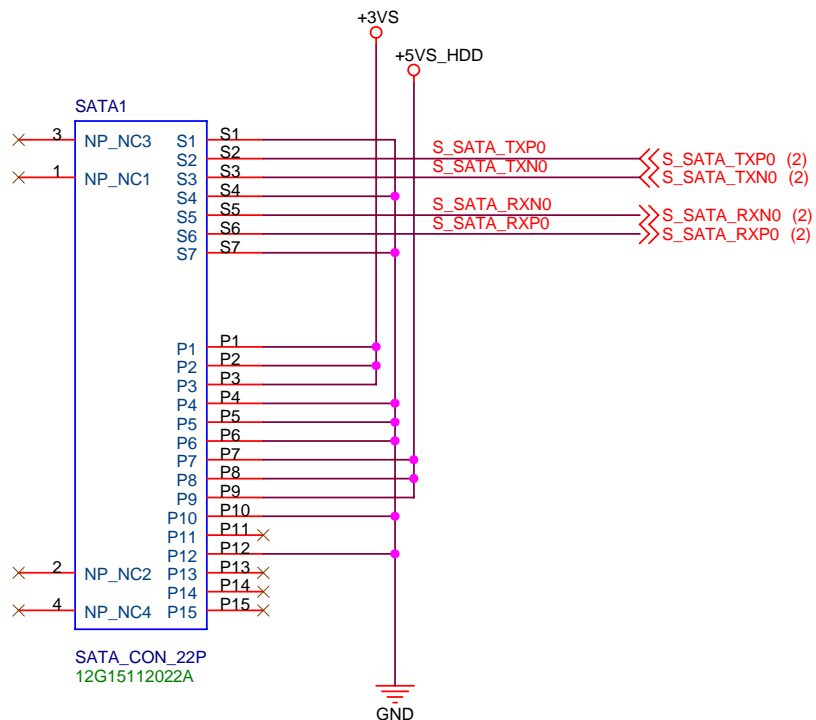


R70 and R71: If don't need retasking function, change to 1K.

MIC_JACK use 12G14040106Q



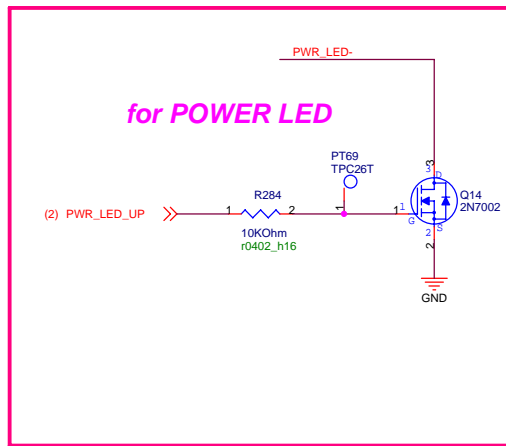
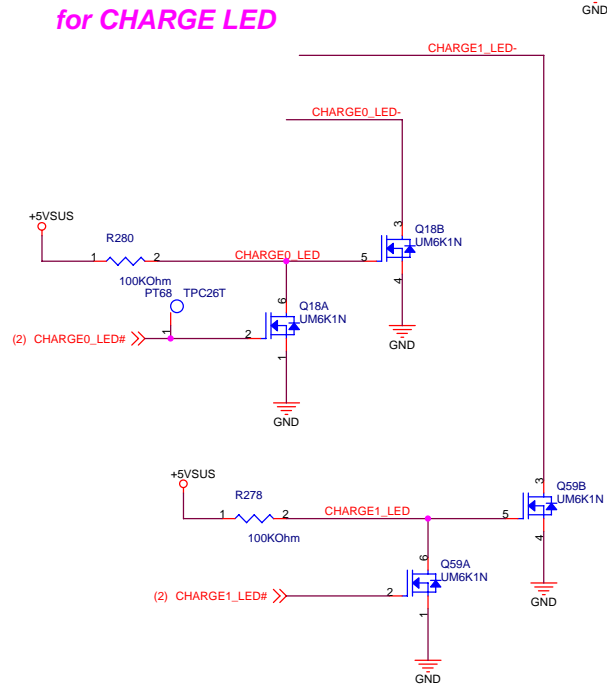
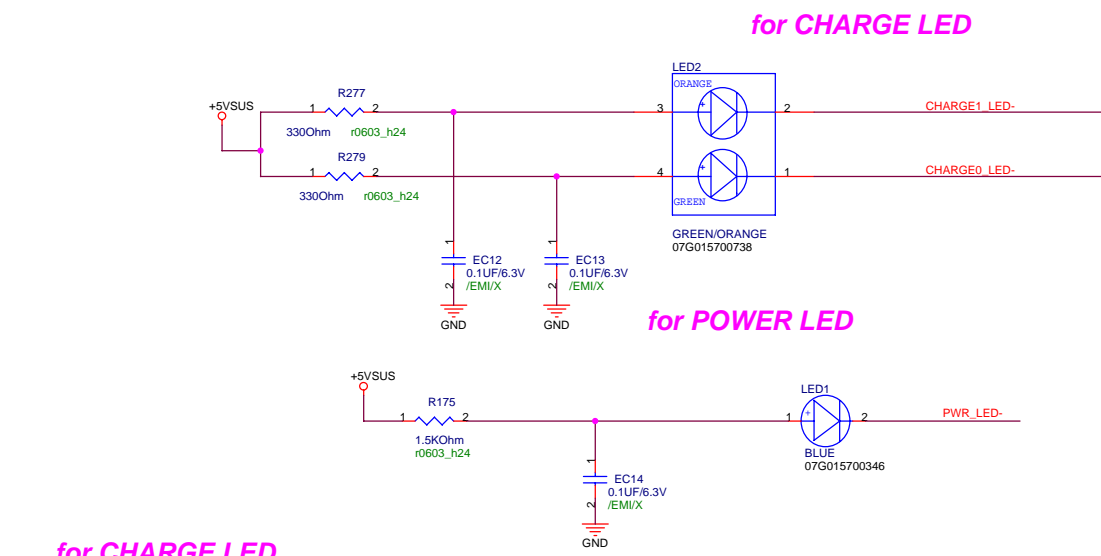
| | | | |
|------------------------------|--------------|------------------|----------|
| <Core Design> | | Title : ALC269-2 | |
| ASUSTek Computer Inc. | | Engineer: MICK | |
| Size A3 | Project Name | MK90H_IO_BOARD | |
| Date: Monday, April 20, 2009 | Sheet 7 | of 12 | Rev 1.1G |



Naming Rule:
IC: IU?
R: IR?
C: IC?
L: IL?

<Core Design>

| | | | |
|---|--------------------------------|-------------------------|-------------|
|  | | Title : HD + Flash Conn | |
| ASUSTek Computer INC. | | Engineer: Kell_Huang | |
| Size A4 | Project Name MK90H_IO_BOARD | | Rev 1.1G |
| Date: Monday, April 20, 2009 | | Sheet 8 of 12 | |

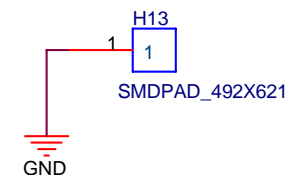
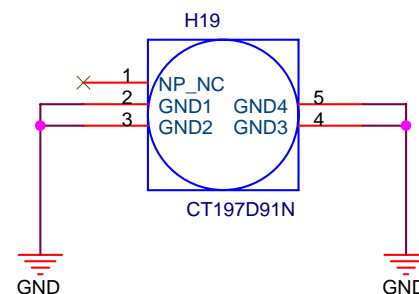
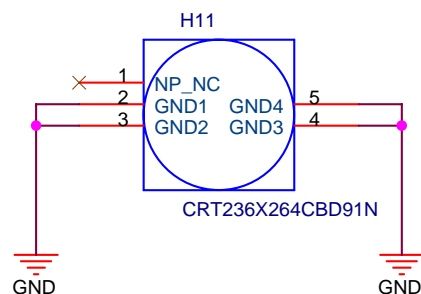
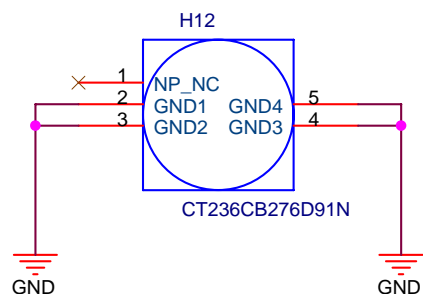
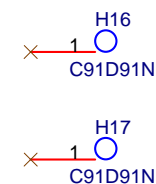
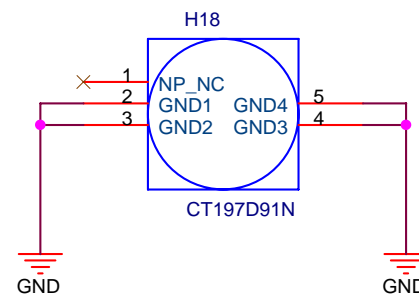
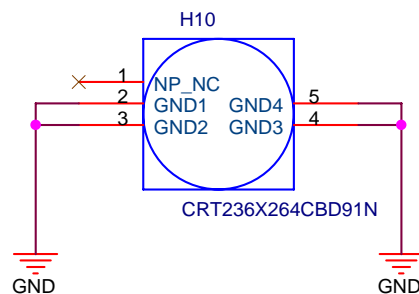
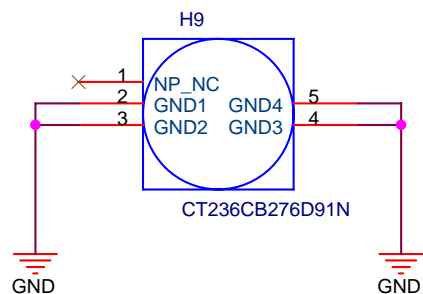


TOP A" , BOT D"


TOP B" , BOT D"

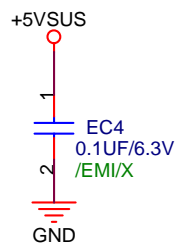
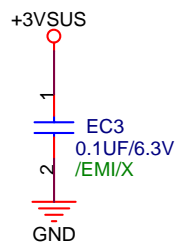
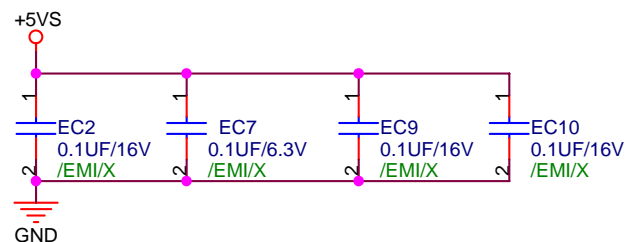
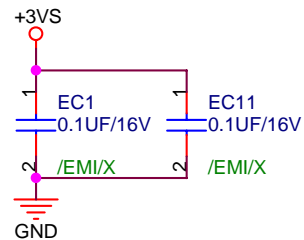
TOP F" , BOT E"

TOP E" , BOT E"




<Core Design>

| | | | |
|---|--------------------------------|--------------------|-------------|
|  | | Title : Screw Hole | |
| ASUSTek Computer INC. | | Engineer: Satan He | |
| Size A | Project Name MK90H_IO_BOARD | | Rev 1.1G |
| Date: Monday, April 20, 2009 | | Sheet 10 | of 12 |



<Core Design>

| | | | |
|---|--------------------------------|---------------------|-------------|
|  | | Title : EMI | |
| ASUSTek Computer INC. | | Engineer: C. A. Lai | |
| Size A | Project Name MK90H_IO_BOARD | | Rev 1.1G |
| Date: Monday, April 20, 2009 | | Sheet | 11 of 12 |

